

## Design Resources

FairLabs has a T620 Dell, Linux based, server capable of running any required EDA or analysis tools to support any IC project a prospective client would need.

This design resource is based on strong experience in installation and use of Linux servers that were designed, loaded, and maintained to support custom IC design. These servers were targeted to support 10 to 15 engineers of many design disciplines in prior engagements.

Knowledge and experience are often required to debug vendor code to make a EDA tool function properly. Every type of EDA and analysis software tool used over the years will sometimes require extra effort to obtain adequate performance. Also PDK's have needed debugging from every foundry we have used in recent years. The software problems can vary from slight to serious. It is prudent to provide lead time to ensure proper setup of design resources for a given project.

We have designed and supported servers that have been used to design and submit twelve layouts to IBM, Silterra, and Tower Jazz over the past ten years. These designs contain power amplifiers, envelope modulators, clocks, bias cells, LNA's, mixers, filters, and much more.

FairLabs is highly experienced in designing, supporting, generating custom IC's both in an academic and commercial environments.